



Chaman Bhardwaj



STORY OF MY LIFE AS A CONSULTANT

IN A DEVELOPED ECONOMY

It was from FEB-08-2019 to MAR-30-2019. The controller box, that I worked on, was at REV C., the Rev D has been shipped. I had to review their schematics and PWB layout for whole week. From 8AM to 5PM I worked with their power supply design engineer, analog design engineer and digital circuit designers. Here it is about my findings; (1) Power Supply designer was focused on power supply portion of the entire system design, (2) Analog circuits designer was focused on his portion of the system design, and (3) Digital circuits designer was focused on only digital circuits of this system.

The manager or else should have been taking care of the system level design aspect. This system prospect should have been working with all three designers as described above.

Now, as you may know, when first prototype was designed and manufactured, let's say it was system designated as **REV A**. This **rev A** have been produced let's say X quantities to accomplish Alpha, beta tests and also DVT tests, on these **rev A** controllers. Some modifications are required based upon test results during alpha, beta and DVT testing. The Rev B was produced to fix all findings during **REV A** cycle. Now **Rev B** has to go through all or selected alpha, Beta and DVT required tests. The **Rev C** was produced, it has to go through all or selected alpha, Beta and DVT all tests too. At this stage of **REV C** of the system, Chaman Bhardwaj was hired to analyze the EMC performance for North American (FCC-15A) and European CE mark compliance. The **Rev A, Rev B and Rev**

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C, test data for EMC compliance was analyzed by Chaman Bhardwaj. I found there no improvements at all for EMC radiated emissions!

I spent first full week of 40 Hours, Infront of the Manager, power supply, analog and digital designer like a professor / instructor, analyzing the schematics, and PWB on the white board through their projector. I found out the interesting violations of the best design practices of grounding, bonding, filtering and shielding requirements. You can download these guidelines by going to www.microvolt.com and ordering through Paypal.

I recommended the required modifications for EMC compliance, and all those modifications were implemented while I checked/monitored to make sure the modifications are implemented correctly by keeping in mind high frequency effects for MODELS of EMI/EMC filter components. in the third week at their own test lab in downtown Milwaukee, WI. I went to their in-house test lab for pre-scans, there were significant improvements as compared to the test results for REV C that Eaton company had in their data base. the power supply design engineer did soldering and modifications of their EMI/EMC components/filters values I recommended to Power supply designer. The Fourth, week the product was taken to external NAVLAP approved, test lab that Eaton Company (EC) had used. We spent about three weeks at this test lab in Illinois to do A to B comparisons with different several modifications to REV C system. During these three to four weeks at test lab in Illinois I was reporting to the Manger and entire hardware and software design teams, about findings and technical solutions in the form of recommendations to find out happy solutions with input and

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NOTE: This is a very snap shot of decades of my experiences in the high-tech world. In one of the meetings, I made recommendations, can we group the prototype R&D evaluations such a way that we need to produce on **FIVE** prototype instead of six. For example, if all R&D evaluations for DVT that is, design verifications & testing, can be performed on prototypes #1 and #5 only. So, we don't need to do DVT test on all prototypes! This means savings of six months plus more than \$200 million dollars in parts and labor!

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